	DIGIT	ΓAL E	LECTRONICS							
1	Course Title:	DIGITAL	ELECTRONICS							
2	Course Code:	FZK3407	7							
3	Type of Course:	Optional								
4	Level of Course:	First Cyc	ele							
5	Year of Study:	3								
6	Semester:	5								
7	ECTS Credits Allocated:	5.00								
8	Theoretical (hour/week):	3.00								
9	Practice (hour/week):	0.00								
10	Laboratory (hour/week):	0								
11	Prerequisites:	none								
12	Language:	Turkish								
13	Mode of Delivery:	Face to f	ace							
14	Course Coordinator:	Dr. Ögr.	Üyesi CENGİZ AKAY							
15	Course Lecturers:	Dr. Öğr.	Üyesi Cengiz Akay							
16	Contact information of the Course Coordinator:	of the Course cenay@uludag.edu.tr Uludağ Üniversitesi Fizik Bölümü								
17	Website:									
18	Objective of the Course:	Basic concepts and principles of physics is given clear and logical manner								
19	Contribution of the Course to Professional Development:		in digital data processing techniques to students using logic and systems, to introduce digital technologies.							
20	Learning Outcomes:									
		1	Students can analyze a digital electronic circuit at the end of the course and solve a given problem.							
		2	They can design a digital electronic circuit suitable for given problem and apply this circuit.							
		3	They can build and solve digital systems with basic logic circuits.							
		4	Learn about the general structure of microcontrollers.							
		5	They can design digital circuits using microcontrollers.							
		6								
		7								
		8								
		9								
		10								
21	Course Content:									
		Co	ourse Content:							
	Theoretical		Practice							
1	Analog and digital data concepts									
2	An overview of digital circuits									
3	Number systems and conversions									
4	Introduction of logic functions									

5	Processing of the basic theorems expressions	of logic								
6	Digital system analysis with basic circuits	ogic								
7	General Review									
8	Truth table, Maxterm, Minterm expand their conversion to each other	ressions								
9	Logic diagram design									
10	Digital system design with all logic	circuits								
11	Karnaugh maps and simplification expressions	of logical								
12	Microcontrollers									
13	Introduction of Arduino Uno, Mega microcontrollers									
14	Microcontroller applications									
22	Textbooks, References and/or Oth Materials:	er	Mano, M.M., "Digital Design", Prentice Hall, 2002, William Kleitz, "Digital Electronics", Prentice Hall, 1996, Garrod & Borns, "Digital Logic", Saunders College Publishing, 1991							
23	Assesment									
TERM L	LEARNING ACTIVITIES	NUMBE R	WEIGHT							
Midterr	m Exam	1	40.00							
Quiz		0	0.00							
Activit	tes		Number Duration (hour) Total Wor Load (hou							
Theore	etical	ľ	114		2.00	28.00				
	oution of Term (Year) Learning Activersity	rities to	0		0.00	0.00				
		-l -	60!00		4.00	56.00				
Home	HONOTO PINGPEXIAM to Success Gra	ade	0		0.00	0.00				
					0.00	0.00				
Field S	ts rement and Evaluation Techniques itudies	Used in the	d Short aug	stions :	osked in the lesson 0.00	0.00				
	E878/ WORK LOAD TABL	F	1		30.00	30.00				
Others		<u> </u>	0		0.00	0.00				
Final E			1		40.00	40.00				
	Vork Load					154.00				
	vork load/ 30 hr					5.13				
ECTS	Credit of the Course					5.00				
25	1		RNING (UALIFIC		OMES TO PROGI					
	PQ1 PQ2 PQ3 PQ4 PQ5 F	000 007 0	nool nool	DO4 1	2014 12012 1201	DO44 DO45 DO46				

25	CONTRIBUTION OF LEARNING OUTCOMES TO PROGRAMME QUALIFICATIONS															
	PQ1	PQ2	PQ3	PQ4	PQ5	PQ6	PQ7	PQ8	PQ9	PQ1 0	PQ11	PQ12	PQ1 3	PQ14	PQ15	PQ16
ÖK1	3	2	2	0	2	2	4	2	3	3	2	2	0	0	0	0
ÖK2	3	3	2	0	2	3	3	3	2	3	0	2	0	0	0	0
ÖK3	5	5	4	3	2	5	3	0	3	3	0	2	0	0	0	0
ÖK4	5	5	5	3	2	5	3	0	3	4	0	2	0	0	0	0

ÖK5	5	5	5	3	2	4	3	0	3	4	0	2	0	0	0	0
Contrib ution Level:	1 \	very		1	ning C	Objec	1	s P Medi			m Qu 4 Higl	alifica n	itions		y High	