EMBEDDED SYSTEMS										
1	Course Title:	EMBEDDED SYSTEMS								
2	Course Code:	BMB4014								
3	Type of Course:	Optional								
4	Level of Course:	First Cycle								
5	Year of Study:	4								
6	Semester:	8								
7	ECTS Credits Allocated:	5.00								
8	Theoretical (hour/week):	3.00								
9	Practice (hour/week):	0.00								
10	Laboratory (hour/week):	0								
11	Prerequisites:									
12	Language:	Turkish								
13	Mode of Delivery:	Face to face								
14	Course Coordinator:	Prof. Dr. KEMAL FİDANBOYLU								
15	Course Lecturers:									
16	Contact information of the Course Coordinator:	ceydanur@uludag.edu.tr								
17	Website:									
18	Objective of the Course:	To have students comprehend the proper and integrated usage of hardware and software components necessary for embedded systems design through the implemented applications on a discovery kit that has STM32L0 series ARM-based microcontroller unit.								
19	Contribution of the Course to Professional Development:									
20	Learning Outcomes:									
		1	Being informed about the application areas and usage of the embedded systems							
		2	Having understood the cooperational logic of hardware and software components that are available in an embedded system							
		3	Being able to use appropriate programming and debugging techniques and tools for embedded systems software development							
		4	Being able to develop proper driver units to manage some hardware elements							
		5	Being able to design systems that run sequentially, concurrently, and in real-time							
		6	Having implemented application projects of the systems that he or she designed							
		7								
		8								
		9								
		10								
21	Course Content:									
107	Course Content:									
week	Theoretical Practice									

1	Introduction: analog and digital systems; sequential, concurrent and real-time system	s							
2	General-purpose processors: software, input/ouput units, addressing modes								
3	Interrupt service routines (ISR)								
4	Standard single-purpose processors: peripherals								
5	Timer module and counters, Pulse width modulation (PWM)								
6	Analog to digital converters (ADC)								
7	Digital to analog converters (DAC)								
8	Universal synchronous asynchronous recievand transmit (USART)	е							
9	Inter-integrated circuit (I2C) interface								
10	Serial peripheral interface (SPI)								
11	Sequential, concurrent, and controls system design with computation models	S							
12	Custom single-purpose processors: hardwa	e							
13	Memories and interfacing, modern design tools								
14	Application project presentations								
	T 1	- 14	E 1 11 10	, ,		D/ 1			
22	Textbooks, References and/or Other Materials:	1 lı	. Embedded Sys ntroduction, F. V	ahid ar	esign: A Unified D nd T. Givargis, Joh	onanım/Yazılım ın Wiley & Sons,			
Activi	tes		Number		Duration (hour	Total Work Load (hour)			
Theore	etical	Α	RM Processor, 1 128036983	L. D. P	ሃ <u>ድ</u>	16,2,5BN: 978-			
Practic	cals/Labs		0		0.00	0.00			
Self stu	dy and preperation	5	TRJM0367 Refer STM32L0x3 Adva	ence IV anced A	lanual of Ultra-low Arm-based 32-bit I	r-power MCUs			
Homev	works		4		10.00	40.00			
Project	ts.	Įv	уу 5 ПИЗZL0531	C8 MC	30.00	30.00			
Field S			0		0.00	0.00			
Midter	m exams R		0		0.00	0.00			
Others	3		0		0.00	0.00			
Qioizd E	Exams 0	О	.00		10.00	10.00			
Total V	Vork Load					148.00			
Fiotal M	Tixoalknload/30 hr 1	4	0.00			4.93			
ECTS	Credit of the Course					5.00			
	oution of Term (Year) Learning Activities to ss Grade	6	0.00						
Contrib	oution of Final Exam to Success Grade	4	40.00						
Total		1	100.00						
Measu Course	rrement and Evaluation Techniques Used in the	ne							
24	ECTS / WORK LOAD TABLE								
	1								

25	CONTRIBUTION OF LEARNING OUTCOMES TO PROGRAMME QUALIFICATIONS															
	PQ1	PQ2	PQ3	PQ4	PQ5	PQ6	PQ7	PQ8	PQ9	PQ1 0	PQ11	PQ12	PQ1 3	PQ14	PQ15	PQ16
ÖK1	3	1	1	2	0	1	2	0	2	1	1	2	0	0	0	0
ÖK2	4	2	4	5	1	2	2	2	3	0	0	0	0	0	0	0
ÖK3	4	3	2	5	1	5	2	2	3	2	2	0	0	0	0	0
ÖK4	5	3	5	5	3	5	2	2	3	3	2	1	0	0	0	0
ÖK5	5	4	5	3	2	5	2	1	1	2	0	0	0	0	0	0
ÖK6	5	4	2	5	3	5	2	2	3	3	2	1	0	0	0	0
		<u> </u>	LO: L	_earr	ning (Objec	tive	s P	Q: P	rogra	ım Qu	alifica	tions	<u> </u>		
Contrib ution Level:	on j			2	2 low		3	3 Medium		4 High		5 Very High				